



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 3340

SERIAL NUMBER 10/637,848	FILING DATE 08/07/2003 RULE	CLASS 257	GROUP ART UNIT 2814	ATTORNEY DOCKET NO. B-4505NP 620843-6
-----------------------------	---------------------------------------	--------------	------------------------	---

APPLICANTS

Lap-Wai Chow, South Pasadena, CA;

William M. Clark JR., Camarillo, CA;

Gavin J. Harbison, Marina del Rey, CA; James P. Baukus, Westlake Village, CA;

** CONTINUING DATA *****

This appln claims benefit of 60/428,634 11/22/2002 *yes, PC*

** FOREIGN APPLICATIONS *****

NONE, PC

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 11/08/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY CA	SHEETS DRAWING 4	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 6
Verified and Acknowledged	<i>Camacho</i> Examiner's Signature				
	<i>PC</i> Initials				

ADDRESS

36716

LADAS & PARRY

5670 WILSHIRE BOULEVARD, SUITE 2100

LOS ANGELES, CA

90036-5679

TITLE

Use of silicon block process step to camouflage a false transistor

FILING FEE RECEIVED	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)